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PTOL-413A (05-03)

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U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE#13/ly Tuto
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Applicant Initiated Interview Request Form

Application No.: 09 / 404,923 First Named Applicant: Douzy
Examiner: S. Makhdoom Art Unit: 2123 Status of Application: Final Rejection

Tentative Participants:

(1) Examiner Makhdoom (2) Wayne Porter
(3) (4)

Proposed Date of Interview: Proposed Time: (AM/PM)

Type of Interview Requested:

(1) [] Telephonic (2) [X] Personal (3) [] Video Conference

Exhibit To Be Shown or Demonstrated: [] YES [X] NO

If yes, provide brief description:

Issues To Be Discussed

Issues (Rej., Obj., etc)	Claims/ Fig. #s	Prior Art	Discussed	Agreed	Not Agreed
(1) Rej.	1, 2, 8, 9	US 6,064,247; US 5,701,441	[]	[]	[]
(2) Rej.	3, 4, 7	"	[]	[]	[]
(3) Rej.	10-13, 17, 18	"	[]	[]	[]
(4) Rej.	19, 20	"	[]	[]	[]

[X] Continuation Sheet Attached

Brief Description of Arguments to be Presented:

See attached

An interview was conducted on the above-identified application on

NOTE:

This form should be completed by applicant and submitted to the examiner in advance of the interview (see MFEP § 713.01).

This application will not be delayed from issue because of applicant's failure to submit a written record of this interview. Therefore, applicant is advised to file a statement of the substance of this interview (37 CFR 1.133(b)) as soon as possible.

H. P. 42,084 6/5/03
(Applicant/Applicant's Representative Signature)

(Examiner/SPE Signature)

This collection of information is required by 37 CFR 1.133. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 21 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing this form, call 1-800-PTO-9199 and select option 2.

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Attachment to PTOL-413A

As to claims 1, 2, 8, 9:

- Table 1 of U.S. Patent 6,064,247 (Krakirian) is not a look up table as recited by claims 1 or 8.
- Krakirian and U.S. Patent 5,701,441 (Trimberger) fail to show stopping emulation by stopping the base clock signal, wherein the index to the look up table is stopped at a stopping point in a clock cycle of the derived clock signal and the derived clock signal does not continue to a subsequent transition before stopping.

As to claims 3, 4, 7:

- Krakirian "frequency control module" 600 is identified by the Office Action as a frequency divider coupled to receive the base clock signal, but the Office Action has not identified a portion of the Krakirian circuit constituting a counter. Krakirian does not describe or suggest coupling a counter to the frequency control module 600 output.
- Krakirian Table 1 is not a look up table as recited by claim 3; even if it were, Krakirian does not describe or suggest a look up table coupled so as to receive the output of a counter circuit which in turn receives the output of the frequency control module 600.
- (As to claim 4) Krakirian Fig. 6A does not show a plurality of additional clock generation circuits coupled in parallel to generate a plurality of additional derived clock signals.

As to claims 10-13, 17, 18:

- Krakirian Table 1 is not a look up table as recited by claim 10; even if it were, the Office Action has not identified a counter providing a sequence of indexes to that table.
- (As to claim 11) Krakirian Table 1 does not show each entry in the table containing a plurality of signal values, with each of the plurality of signal values corresponding to a different one of a plurality of derived clock signals.
- (As to claim 12) Krakirian does not describe or suggest the output of frequency control module 600 being provided to a counter.
- (As to claim 13) The Office Action asserts that Krakirian Fig. 8 shows a multiplexor, with the first clock signal C4h and the second signal Cin being

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multiplexed to the output signal C4; Krakirian does not describe or suggest providing the C4 signal to a counter.

- (As to claim 17) There is no requirement that a counter always include three bits (see MPEP § 2131.01(III)).

As to claims 19, 20:

- Krakirian Table 1 is not a look up table as recited by claim 19.



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